## YEAR 5 MAI, 2019/20 - ELECTRONIC/COMPUTER ENGINEERING

**Date of issue:** 24th October, 2019

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<th>DAY</th>
<th>SEMESTER</th>
<th>0900 - 1000</th>
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<td><strong>Second semester</strong></td>
<td>CS7NS1 [LB01]</td>
<td>SC7IS4 [LB08]</td>
<td>4C8 [LB04]</td>
<td>4C8 [LB04]</td>
<td>CS7NS5 [LB01]</td>
<td>CS7DS2 [LB08]</td>
<td>4C8 [LB08]</td>
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**Notes:**
- Week 3 to 10
- Cad Lab weeks 4, 6, 8, 10, 12
- Cad Lab weeks 4, 6, 8, 10, 12
- Design Loft/PBSR1/2
- Design Loft/PBSR1/2
- Design Loft/PBSR1/2

**Attendance:**
- Group A: LB2.02
- Group B: L2.01

**Lecturers:**
- CS7NS1:
  - [HAM1]
- CS7IS1:
  - [HAM2]
- CS7CS3:
  - [CAD LAB]
- CS7DS2:
  - [CAD LAB]
- CS7CS3:
  - [CAD LAB]
- CS7DS4:
  - [CAD LAB]

**Rooms:**
- LB04
- LB08
- LB1.07
- LB01
- LG37
- HAM1
- HAM2
- HAM3
- PBSR1
- PBSR2

**Schedules:**
- [18.00 - 19.00]
- [17.00 - 18.00]
- [16.00 - 17.00]
- [15.00 - 16.00]
- [14.00 - 15.00]
- [13.00 - 14.00]
- [12.00 - 13.00]
- [11.00 - 12.00]
- [10.00 - 11.00]
- [09.00 - 10.00]
**FURTHER INFORMATION ON YEAR 5 MAI ENGINEERING TIMETABLE, 2019/20**

### Module Codes
- SE1 = SE1  Engineering Research Project [25 credits]
- SE2 = ESE2  Research Methods [5 credits]
- SE3 = MESE3  Innovation in Product Development [15 credits]

### Electronic Engineering Modules
- SB9 = ME809  Control Engineering II [5 credits]
- 4C1 = EEE4C01  Integrated Systems Design [5 credits]
- 4C2 = EEE4C02  Microelectronic Circuits [5 credits]
- SC1 = EEE5C01  Motion Picture Engineering [10 credits]
- SC3 = EEE5C03  Statistical Signal Processing [5 credits]
- SC4 = EEE5C04  Speech and Audio Engineering [5 credits]
- SC7 = EEE5C07  Complex Systems Science for Communication Networks [5 credits]
- SC16 = EEU5C16  Deep Learning and its Applications [10 credits]
- EEMT07 = EEMT07  Audio Production Engineering
- EEMT16 = EEMT16  Audio Production Techniques [5 credits]
- EEMT17 = EEMT17  Spatial Audio [5 credits]
- SM05 = EEP5S05  Optimisation & Control

### Computer Engineering Modules
- CSU55001 = CS4501  Fuzzy Logic and Control Systems [5 credits]
- CSU55004 = CS55004  Formal Verification (CS5403) [5 credits]
- CS7CS3 = CS7CS3  Advanced Software Engineering [10 credits]
- CS7CS4 = CS7CS4  Machine Learning [5 credits]
- CS7DS2 = CS7DS2  Optimisation Algorithms for Data Analysis [5 credits]
- CSU44056 = CS7DS4  Data Visualisation [5 credits]
- CS7GV2 = CS7GV2  Mathematics of Light and Sound [5 credits]
- CS7GV3 = CS7GV3  Real-time Rendering [5 credits]
- CS7GV5 = CS7GV5  Real-time Animation [5 credits]
- CS7IS1 = CS7IS1  Knowledge and Data Engineering [5 credits]
- CS7IS2 = CS7IS2  Artificial Intelligence [5 credits]
- CS7IS3 = CS7IS3  Information Retrieval and Web Search [5 credits]
- CS7IS4 = CS7IS4  Text Analytics [5 credits]
- CS7IS5 = CS7IS5  Adaptive Applications [5 credits]
- CS7NS1 = CS7NS1  Scalable Computing/Internet Applications [5 credits]
- CS7NS2 = CS7NS2  Internet of Things [5 credits]
- CS7NS4 = CS7NS4  Urban Computing [5 credits]
- CS7NS5 = CS7NS5  Security and Privacy [5 credits]
- CS7NS6 = CS7NS6  Distributed Systems [5 credits]

### Venues
- 4050A = Arts Building, 4th Floor, room 4050A
- AP2.03/2.04 = Room 2.03/2.04, Second Floor, Arts an Phaiarsaigh
- CADLAB = Room 2.28, Second Floor, Aras an Phaiarsaigh
- CLT = Crossland Lecture Theatre, Parsons Building
- DESIGN LOFT = Design Loft, Top Floor, Parsons Building
- HAM1 = Salmon Theatre, Hamilton Building
- HAM2 = Synge Theatre, Hamilton Building
- HAM3 = MacNeill Theatre, Hamilton Building
- HAM4 = Joly Theatre, Hamilton Building
- GLT = Geography/Haughton Lecture Theatre, Museum Building
- GOLDHALL = Goldsmith Hall, Pearse Street
- ICTLAB1 = Ground Floor, ICT Hut
- L2.01 = PC Lab 2.01, 2nd floor, Trinity Central (beside TBSI)
- L2.02 = PC Lab 2.02, 2nd floor, Trinity Central (beside TBSI)
- L2B1 = Lecture Theatre 01, Lower Ground Floor, Lloyd Institute
- LB04 = Lecture Theatre 04, Lower Ground Floor, Lloyd Institute
- LB08 = Lecture Theatre 08, Lower Ground Floor, Lloyd Institute
- LG12 = Lab 12, Lower Ground Floor, O’Reilly Institute
- LG37 = Lab 137, Lower Ground Floor, O’Reilly Institute
- PBSR1/2 = Seminar Room 1/2, Parson’s Building
- SLS4.09 = Room 4.09, Fourth Floor, 7-9 South Leinster Street
- SS LAB = Room 2.02, Second Floor, Aras an Phaiarsaigh
- RH = Regent House

### Notes
- Electronic Engineering students are required to take SE1 and SE2 and a further 30 credits (with at least 15 in semester 1) from EE4C01, EE4C02, EESC01, EESC03, EESC04, EESC07, EESC16, CS7DS2, CS7NS2, EEMT16, EEMT17 and EESB09
- Computer Engineering students are required to take SE1 and SE2 as well as 30 credits from CS4501, CS4504, CS4521, CS7CS3, CS7CS4, CS7DS2, CS7DS4, CS7GV3, CS7GV5, CS7IS1, CS7IS2, CS7IS4, CS7IS5, CS7IS6, EEE4C01, EE4C02, EESC01, EESC03, EESC04, EESC07, EESC16, EEMT16 and EEMT17
- Electronic/Computer Engineering students are required to take SE1 and SE2 as well as 30 credits (with at least 10 EE-coded and at least 10 CS-coded modules) from CS4501, CS4504, CS4521, CS7CS3, CS7CS4, CS7DS2, CS7DS4, CS7GV3, CS7GV5, CS7IS1, CS7IS2, CS7IS4, CS7IS5, CS7IS6, EE4C01, EE4C02, EESC01, EESC03, EESC04, EESC07, EESC16, EEMT16 and EEMT17

### Semester dates:
- First semester: Monday, 9th September, 2019 to Friday, 29th November, 2019
- Second semester: Monday, 20th January, 2020 to Friday, 10th April, 2020

### Project/Assignment Weeks:
- First semester: Monday, 21st October 2019 to Friday, 25th October 2019
- Second semester: Monday, 2nd March 2020 to Friday, 6th March 2020

### Draft examination dates (to be confirmed):
#### Semester 1 examinations:
- Monday, 9th December, 2019 to Friday, 13th December, 2019
- (Contingency Days: December 16, 17, 2019)

#### Semester 2 examinations:
- Monday, 27th April, 2020 to Saturday, 2nd May, 2020
- (Contingency Days: April 23, 24, 25, 2020)

#### Supplemental examinations:
- August 31, September 1, 2, 3, 4, 2020
- (Contingency Days: August 24, 25, 26, 27 and 28, 2020)