

YEAR 5 MAI, 2018/19 - ELECTRONIC/COMPUTER ENGINEERING

Date of issue: 29th January 2019

DAY	SEMESTER	0900 - 1000	1000 - 1100	1100 - 1200	1200 - 1300	1300 - 1400	1400 - 1500	1500 - 1600	1600 - 1700	1700 - 1800	18.00 - 19.00
MONDAY	First semester	5C16 [M20]	CS7CS4 [LB04] (Week 3 to 10)	CS7NS1 [LB04]				CS7NS2 [LB1.07]	5C3 [M20]	CS4521 [HAM1]	
	Second semester		EEMT17 [MMT LAB]		5C1 [CAD LAB]	5C7 [M21]	EEMT16 [MMT LAB]		CS7NS5 [LB04]		
			CS7NS6 [SLS4.09]				CS7IS4 [LB08]	CS7IS4 [LB08]	4C2 [PBSR1]		
TUESDAY	First semester	CS4501 [LB01]		5B9 [CLT]			5C3 [M20]	CS7CS4 [HAM3]	CS7IS1 [LB1.07]	CS4504 [HAM1]	
	Second semester	CS7GV5 [LB01]	CS7NS6 [SLS4.09]			CS7GV3 [LB1.20]	5C7 laboratory session (weeks 3, 5, 9, 10) [SS LAB]		CS7IS5 [LB01]		
				5C1 laboratory session [CADLAB]			5C7 [M21]	CS7CS3 [LB01]		4C1 laboratories [AP2.28]	
						4C2 [PBSR1]					
WEDNESDAY	First semester		5B9 [CLT]	5C4 [AP2.03]		CS7NS2 [HAM1]	5E2 [GOLDHALL]	5E2 [HAM3]	5E3 [DESIGN LOFT/PBSR1/2]		
		5C16 [CADLAB]			CS7CS4 [HAM4]				5C16 [M21]	CS7CS4 [LB04]	
Second semester	5C7 [AP2.04]	5C1 [CADLAB]	CS7GV5 [SLS4.09]	4C2 [AP2.04]		CS7CS3 [LB04]	CS7DS2 [LB08]	4C2 [PBSR2]	4C1 (Week 12) [M20]		
									5E3 [DESIGN LOFT/PBSR1]		
THURSDAY	First semester	5C4 [AP2.03/CAD LAB]		CS4501 [LB08]	5C3 [M20]	CS4501 [HAM2]	CS7NS1 [LB08]	5C3 [M20]	CS4504 [LB01]		
			5B9 [CLT]	5C16 [LTEE2]				CS4521 [LB1.20]			
	Second semester	CS7NS5 [LB01]	CS7IS2 [LB04]	5C1 [CADLAB]	CS7NS4 [LB08]		CS7DS2 [LB01]	CS7CS3 [LB01]			
FRIDAY	First semester	CS4521 [LB1.20]				CS7IS3 [LB1.07]	CS4504 [LB1.07]	CS4521 [LB08]	CS7GV2 [SLS4.09]		
								CS7GV2 [SLS4.09]	5E3 [DESIGN LOFT/PBSR1/2]		
	Second semester		CS7IS2 [LB04]	5C7 [M21]	5C1 [M21]	CS7NS4 [LB1.07]	CS7IS5 [LB1.07]	CS7DS4 [LB01]		4C1 (Weeks 1 - 11) [AP2.03]	
			CS7NS6 [SLS4.09]				5E3 [DESIGN LOFT/PBSR2]				

FURTHER INFORMATION ON YEAR 5 MAI ENGINEERING TIMETABLE, 2018/19

Module Codes

5E1 = 5E1 Engineering Research Project [25 credits]
 5E2 = CE5E2 Research Methods [5 credits]
 5E3 = ME5E3 Innovation in Product Development [15 credits]

Electronic Engineering Modules

5B9 = ME5B09 Control Engineering II [5 credits]
 4C1 = EEU44C01 Integrated Systems Design [5 credits]
 4C2 = EEU44C02 Microelectronic Circuits [5 credits]
 5C1 = EE5C01 Motion Picture Engineering [10 credits]
 5C3 = EE5C03 Statistical Signal Processing [5 credits]
 5C4 = EE5C04 Speech and Audio Engineering [5 credits]
 5C7 = EE5C07 Complex Systems Science for
 Communication Networks [5 credits]
 5C16 = EE5C16 Deep Learning and its Applications [10 credits]
 EEMT16 = EEMT16 Audio Production Techniques [5 credits]
 EEMT17 = EEMT17 Spatial Audio [5 credits]

Computer Engineering Modules

CS4501 = CS4501 Fuzzy Logic and Control Systems [5 credits]
 CS4504 = CS4503 Formal Verification [5 credits]
 CS4521 = CS4521 Advanced Computer Architecture [5 credits]
 CS7CS3 = CS7CS3 Advanced Software Engineering [10 credits]
 CS7CS4 = CS7CS4 Machine Learning [5 credits]
 CS7DS2 = CS7DS2 Optimisation Algorithms for
 Data Analysis [5 credits]
 CS7DS4 = CS7DS4 Data Visualisation [5 credits]
 CS7GV2 = CS7GV2 Mathematics of Light and Sound [5 credits]
 CS7GV3 = CS7GV3 Real-time Rendering [5 credits]
 CS7GV5 = CS7GV5 Real-time Animation [5 credits]
 CS7IS1 = CS7IS1 Knowledge and Data Engineering [5 credits]
 CS7IS2 = CS7IS2 Artificial Intelligence [5 credits]
 CS7IS3 = CS7IS3 Information Retrieval and Web Search [5 credits]
 CS7IS4 = CS7IS4 Text Analytics [5 credits]
 CS7IS5 = CS7IS5 Adaptive Applications [5 credits]
 CS7NS1 = CS7NS1 Scalable Computing [5 credits]
 CS7NS2 = CS7NS2 Internet of Things [5 credits]
 CS7NS4 = CS7NS4 Urban Computing [5 credits]
 CS7NS5 = CS7NS5 Security and Privacy [5 credits]
 CS7NS6 = CS7NS6 Distributed Systems [5 credits]

Venues

AP2.03/2.04 = Room 2.03/2.04, Second Floor, Aras an Phiarsaigh
 CADLAB = Room 2.28, Second Floor, Aras an Phiarsaigh
 CLT = Crossland Lecture Theatre, Parsons Building
 DESIGN LOFT = Design Loft, Top Floor, Parsons Building
 HAM1 = Salmon Theatre, Hamilton Building
 HAM2 = Synge Theatre, Hamilton Building
 HAM3 = MacNeill Theatre, Hamilton Building
 HAM4 = Joly Theatre, Hamilton Building
 GOLDHALL = Goldsmith Hall, Pearse Street
 ICTLAB1 = ICTLAB1, Ground Floor, ICT Hut
 LB1.07 = Room 1.07, First Floor, Lloyd Institute
 LB1.20 = Room 1.20, First Floor, Lloyd Institute
 LB01 = Lecture Theatre 01, Lower Ground Floor, Lloyd Institute
 LB04 = Lecture Theatre 04, Lower Ground Floor, Lloyd Institute
 LB08 = Lecture Theatre 08, Lower Ground Floor, Lloyd Institute
 LG12 = Lab 12, Lower Ground Floor, O'Reilly Institute
 LG37 = Lab 137, Lower Ground Floor, O'Reilly Institute
 LTEE2 = Lecture Theatre 2, Lower Basemewnt, Panoz Institute
 M20 = Lecture Theatre M20, First Floor, Museum Building
 M21 = Lecture Theatre M21, First Floor, Museum Building
 MMT LAB = MMT Lab, Stack B, IFSC, Custom House Quay
 PBSR1/2 = Seminar Room 1/2, Parsons Building
 SLS4.09 = Room 4.09, Fourth Floor, 7 - 9 South Leinster Street
 SS LAB = Room 2.02, Second Floor, Aras an Phiarsaigh

Notes

Electronic Engineering students are required to take 5E1 and 5E2 and a further 30 credits (with at least 15 in semester 1) from EE4C01, EE4C02, EE5C01, EE5C03, EE5C04, EE5C07, EE5C16, CS7DS2, CS7NS2, EEMT16, EEMT17 and ME5B09

Computer Engineering students are required to take 5E1 and 5E2 as well as 30 credits from CS4501, CS4504, CS4521, CS7CS3, CS7CS4, CS7DS2, CS7DS4, CS7GV2, CS7GV3, CS7GV5, CS7IS1, CS7IS2, CS7IS3, CS7IS4, CS7IS5, CS7NS1, CS7NS2, CS7NS4, CS7NS5, EE5C01 and EE5C16

Electronic/Computer Engineering students are required to take 5E1 and 5E2 as well as 30 credits (with at least 10 EE-coded and at least 10 CS-coded modules) from CS4501, CS4504, CS4521, CS7CS3, CS7CS4, CS7DS2, CS7DS4, CS7GV3, CS7GV5, CS7IS1, CS7IS2, CS7IS3, CS7NS1, CS7NS2, CS7NS5, CS7NS6, EE4C01, EE4C02, EE5C01, EE5C03, EE5C04, EE5C07, EE5C16, EEMT16 and EEMT17

Semester dates:

First semester: Monday, 10th September, 2018 to Friday, 30th November, 2018
 Second semester: Monday, 21st January, 2019 to Friday, 12th April, 2019

Project/Assignment Weeks:

First semester: Monday, 22nd November, 2018 to Friday, 26th November, 2018
 Second semester: Monday, 4th March, 2019 to Friday, 8th March, 2019

Draft examination dates (to be confirmed):

Semester 1 examinations: Saturday, 8th December, 2018 to Friday, 14th December, 2018
 Semester 2 examinations: Tuesday, 23rd April, 2019 to Friday, 3rd May, 2019