

JUNIOR SOPHISTER ENGINEERING, 2018/19 - ELECTRONIC AND COMPUTER ENGINEERING

Date of issue: 21st January 2019

DAY	SEMESTER	0900 - 1000	1000 - 1100	1100 - 1200	1200 - 1300	1300 - 1400	1400 - 1500	1500 - 1600	1600 - 1700	1700 - 1800
MONDAY	<i>First semester</i>		3E3 [2043]	3D1 [LB08]	3D1 lab session [LG36]		3D5A [M20]	3C2 [CLT]	3E1 [HAM4]	3D1 lab session [LG36]
	<i>Second semester</i>	3C7 [M21]	3C9 laboratory sessions [UG LAB]					3D2 [LB04]	3C5 [HAM5]	3D4 [LB01]
			3D2 [LB04]		3D3 [LB01]					
TUESDAY	<i>First semester</i>	3D5A [M20]	3C1 [2039]	3C2 [M21]	3D1 [M17]		Electronic Engineering Laboratory sessions [CADLAB]			
	<i>Second semester</i>	3C7 [LB08]	3D5B tutorial [LB1.07]		3D4 [LB01]		Electronic/Computer Engineering Laboratory sessions [CADLAB, UG LAB and LG35, LG36]			
WEDNESDAY	<i>First semester</i>	3E1 tutorial [CEDR and DO]	3E3 [2043]		3D5A [LB01]					
	<i>Second semester</i>	3E4 [LB08]	3C5 [M21]		3E4 tutorials [RH]		3E4 [LB08]	3D2 laboratory sessions [LG35/LG36]		3D4 [LB01]
			3D4 lab session [LG12]							
THURSDAY	<i>First semester</i>		3C2 [M21]	3C1 [M20]			3E3 [2039]	3D5A tutorial [M20]	3E1 [2039]	3C1 [HAM4]
	<i>Second semester</i>		3C5 [HAM2]	3C3 [CLT]				3C7 laboratory session [CADLAB]		
			3D2 [LB08]	3D3 [LB04]						
FRIDAY	<i>First semester</i>	3E1 [2043]	3C2 [M17]	3C1 [2039]	3E3 [2039]		3D5A laboratory sessions [LG12]			
	<i>Second semester</i>	3C3 [CLT]		3D2 [LB04]		3C5 [M20]	3D5B laboratory sessions [LG12]			
			3D3 [LB08]					3C8 laboratory sessions [CADLAB, UG LAB]		

FURTHER INFORMATION ON JUNIOR SOPHISTER ENGINEERING TIMETABLE, 2018/19

Module codes:

3E1 = MA3E1 Engineering Mathematics V [5 credits]
 3E3 = EEU33E03 Probability and Statistics [5 credits]
 3E4 = CEU33E04 Innovation and Entrepreneurship
 for Engineers [5 credits]

3C1 = EEU33C01 Signals and Systems [5 credits]
 3C2 = EEU33C02 Digital Circuits [5 credits]
 3C3 = EEU33C03 Analogue Circuits [5 credits]
 3C5 = EEU33C05 Telecommunications [5 credits]
 3C7 = EEU33C07 Digital Systems Design [5 credits]
 3C8 = EEU33C08 Digital Circuits Design* [5 credits]
 3C9 = EEU33C09 Analogue Circuits Design* [5 credits]

3D1 = CS3D1 Microprocessor Systems 1 [5 credits]
 3D2 = CS3D2 Microprocessor Systems 2 [5 credits]
 3D3 = CS3D3 Computer Networks [5 credits]
 3D4 = CS3D4 Operating Systems and Concurrent Systems [5 credits]
 3D5A = CS3D5A Data Structures and Algorithms [5 credits]
 3D5B = CS3D5B Software Design and Implementation* [5 credits]

* 3C8, 3C9 and 3D5B are based entirely on project work

Venues:

2039 = JM Syngé Lecture Theatre, Arts Building
 2043 = Thomas Davis Theatre, Arts Building
 AP2.03 = Room 2.03, Second Floor, Aras an Phiarsaigh
 CADLAB = Room 2.28, Second Floor, Aras an Phiarsaigh
 CEDR = Civil Engineering Demo. Room, Simon Perry Building
 CHSCLT = Science Lecture Theatre, Chemistry Building
 CLT = Crossland Theatre, Parsons Building
 DO = Drawing Office, Top Floor, Museum Building
 HAM1 = Salmon Lecture Theatre, Hamilton Building
 HAM2 = Syngé Lecture Theatre, Hamilton Building
 HAM3 = MacNeill Theatre, Hamilton Building
 HAM4 = Joly Theatre, Hamilton Building
 HAM5 = Maxwell Theatre, Hamilton Building
 LB01 = Lecture Theatre 01, Lower Basement, Lloyd Institute
 LB04 = Lecture Theatre 04, Lower Basement, Lloyd Institute
 LB08 = Lecture Theatre 08, Lower Basement, Lloyd Institute
 LB1.07 = Room 1.07, First Floor, Lloyd Institute
 LG12/35/36 = PC Room 12/35/36, Lower Ground Floor, O'Reilly Institute
 M17 = Museum 17, First Floor, Museum Building
 M20 = Museum 20, First Floor, Museum Building
 M21 = Museum 21, First Floor, Museum Building
 RH = Regent House, Top Floor, Front Gate
 UG LAB = Room 2.15, Second Floor, Aras an Phiarsaigh

Semester dates:

First semester: Monday, 10th September, 2018 to Friday, 30th November, 2018
 Second semester: Monday, 21st January, 2019 to Friday, 12th April, 2019

Project/Assignment Weeks:

First semester: Monday, 22nd November, 2018 to Friday, 26th November, 2018
 Second semester: Monday, 4th March, 2019 to Friday, 8th March, 2019

Draft examination dates (to be confirmed):

Semester 1 examinations: Saturday, 8th December, 2018 to Friday, 14th December, 2018
 Semester 2 examinations: Tuesday, 23rd April, 2019 to Friday, 3rd May, 2019
 Supplemental examinations: Monday, 26th August, 2019 to Saturday, 31st August, 2019

Electronic Engineering Modules

3E1; 3E3; 3E4 (or Broad Curriculum module); 3C1; 3C2; 3C3; 3C5; 3C7; 3C8; 3C9; 3D1 and 3D5A

Computer Engineering Modules

3E1; 3E3; 3E4 (or Broad Curriculum module); 3C1; 3C2; CS2022; 3D1; 3D2; 3D3; 3D4, 3D5A and 3D5B

Electronic/Computer Engineering Modules

3E1; 3E3; 3E4 (or Broad Curriculum module); 3C1; 3C2; 3C5; 3C7; 3C8; 3D1; 3D2; 3D3 and 3D5A