<table>
<thead>
<tr>
<th>Module Code</th>
<th>EEU22E10</th>
</tr>
</thead>
<tbody>
<tr>
<td>Module Name</td>
<td>Engineering Design IV : Project</td>
</tr>
<tr>
<td>ECTS Weighting(^1)</td>
<td>10 ECTS</td>
</tr>
<tr>
<td>Semester taught</td>
<td>Semester 2</td>
</tr>
</tbody>
</table>
| Module Coordinator/s | Assistant Prof. François Pitié ([pitief@tcd.ie](mailto:pitief@tcd.ie))  
                        Assistant Prof. Glenn Strong ([Glenn.Strong@scss.tcd.ie](mailto:Glenn.Strong@scss.tcd.ie))  
                        Kerstin Ruhland ([Kerstin.Ruhland@scss.tcd.ie](mailto:Kerstin.Ruhland@scss.tcd.ie)) |

**Module Learning Outcomes** with reference to the Graduate Attributes and how they are developed in discipline

On completion of this module the student will be able to:

LO1. Apply the engineering process of problem solving.

LO2. Design a simple autonomous vehicle to meet a well-defined specification.

LO3. Clearly demonstrate group working, including task sub-division and integration of individual contributions from the team.

LO4. Plan a project, meeting all interim deliverables

LO5. Implement project tracking and code version control.

LO6. Apply knowledge of the health and safety requirements of electronic circuit board construction.

LO7. Recognise issues to be addressed in a combined hardware and software system design.

LO8. Develop skills in the areas of quantitative analysis, scientific reasoning and communication.

LO9. Develop practical experimental skills in electronic circuit testing.

LO10. Develop practical experimental skills in software system testing.

LO11. Explore and defend their design decisions, critique their own design.

LO12. Evaluate the outcome of their achievements given the original specification.

LO13. Demonstrate organised and concise report writing skills.
Graduate Attributes: levels of attainment
To act responsibly - Enhanced
To think independently - Introduced
To develop continuously - Enhanced
To communicate effectively - Enhanced
## Module Content

The objectives of this Module are:

- To apply basic principles of science and engineering to conceive, design,
- Implement and operate (CDIO) an autonomous vehicle
- To introduce group working and project planning
- To introduce the principle of circuit construction and the health and safety issues associated with electronic circuit construction and the adoption of test procedures
- To introduce the principles of software systems design including user interface design and control software for wireless communications
- To analyse the design and optimise it with respect to manufacturing and testing
- To introduce the requirements of project documentation, circuit drawings and software documentation
- To introduce project reporting and presentation

## Teaching and Learning Methods

The module is taught using a combination of lectures, demonstration laboratories and through project sessions at which advisors are present. As a 10 ECTS course, the average individual student effort should be 200-250 hours spread over the semester. 55 of these will be actual contact hours. Thus all students, as individuals and as groups, are also expected to undertake extensive independent research and development work on the project.
### Assessment Details

Please include the following:

- Assessment Component
- Assessment description
- Learning Outcome(s) addressed
- % of total
- Assessment due date

<table>
<thead>
<tr>
<th>Assessment Component</th>
<th>Assessment Description</th>
<th>LO Addressed</th>
<th>% of total</th>
<th>Week due</th>
</tr>
</thead>
</table>

| Reassessment Requirements |

| Contact Hours and Indicative Student Workload |

- **Contact hours:** 55
- **Independent Study (preparation for course and review of materials):**
- **Independent Study (preparation for assessment, incl. completion of assessment):**

### Recommended Reading List

### Module Pre-requisite

### Module Co-requisite

### Module Website


### Are other Schools/Departments involved in the delivery of this module? If yes, please provide details.

| Module Approval Date |

| Approved by |

| Academic Start Year |

September 9th 2019
| Academic Year of Date | 2019/2020 |