## SENIOR SOPHISTER ENGINEERING, 2017/18 - ELECTRONIC AND COMPUTER ENGINEERING

**Date of issue: 24th August, 2017**

<table>
<thead>
<tr>
<th>DAY</th>
<th>SEMESTER</th>
<th>0900 - 1000</th>
<th>1000 - 1100</th>
<th>1100 - 1200</th>
<th>1200 - 1300</th>
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<tbody>
<tr>
<td>FRIDAY</td>
<td>First semester</td>
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<td>Electronic Engineering laboratories/projects (C and CD stream) [EE LABS]</td>
<td>4C5 [M20]</td>
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<tr>
<td>Second semester</td>
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<td>Electronic Engineering laboratories/projects (C and CD stream) [EE LABS]</td>
<td>4C8 [M21]</td>
<td>CS4406 [LB01]</td>
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Course Codes

**Module Codes**

4E1 = EE4E1/CS4E1  Management for Engineers [5 credits]
4E2 = EE4E2/CS4E2  Electronic/Computer Engineering Project [15 credits]*
4E4 = EE/CS4E04  Engineering Project Internship [30 credits]
4B9 = ME4B09  Control Engineering I [5 credits]
4B12 = ME4B12  Acoustics [5 credits]
4C4 = EE4C04  Next Generation Networks [5 credits]
4C5 = EE4C05  Digital Signal Processing [5 credits]
4C7 = EE4C07  Information and Communication Theory [5 credits]
4C8 = EE4C08  Digital Media Processing [5 credits]
4C15 = EE4C15  Analogue Signal Processing [5 credits]
4C16 = EE4C16  Machine Learning with Applications in Media Engineering [5 credits]
CS3421 = CS3421  Computer Architecture II [5 credits]
CS4D2A = CS4D2A  Information Management II [5 credits]
CS4D2B = CS4D2B  Knowledge Engineering [5 credits]
CS4052 = CS4052  Computer Graphics [5 credits]
CS4053 = CS4053  Computer Vision [5 credits]
CS4400 = CS4400  Internet Applications [5 credits]
CS4404 = CS4404  Machine Learning with Applications in Data Analysis [5 credits]
CS4405 = CS4405  Optimisation Algorithms for Data Analysis [5 credits]
CS4406 = CS4406  Data Visualisation [5 credits]
CS4407 = CS4407  Security and Privacy [5 credits]
CS7434 = CS7434  Augmented Reality [5 credits]

**Venues**

2037 = Robert Emmet Lecture Theatre, Arts Building
2039 = JM Synge Theatre, Arts Building
AP2.04 = Room 2.04, Second Floor, Aras an Phiaraisg
AP4.03 = Room 4.03, Fourth Floor, Aras an Phiaraisg
CADLAB = Room 2.28, Second Floor, Aras an Phiaraisg
CLT = Crossland Lecture Theatre, Parsons Building
DO = Drawing Office, Top Floor, Museum Building
EE LABS = Electronic Engineering Labs, Aras an Phiaraisg
HAM4 = Joly Lecture Theatre, Hamilton Building
ICTLAB1 = ICT Laboratory 1, Ground Floor, ICT Hut
ICTLAB2 = ICT Laboratory 2, First Floor, ICT Hut
LB01 = Lecture Theatre 01, Lower Basement, Lloyd Institute
LB04 = Lecture Theatre 04, Lower Basement, Lloyd Institute
LB08 = Lecture Theatre 08, Lower Basement, Lloyd Institute
LB1.07 = Lecture Theatre 1.07, First Floor, Lloyd Institute
M17 = Museum 17, First Floor, Museum Building
M20 = Museum 20, First Floor, Museum Building
M21 = Museum 21, First Floor, Museum Building
MECH LAB = Mechatronics Laboratory, Parsons Building

**Semester Dates**

First semester: Monday, 25th September, 2017 to Friday, 15th December, 2017
Second semester: Monday, 15th January, 2018 to Friday, 6th April, 2018

**Project/Assignment Weeks**

First semester: Monday, 6th November, 2017 to Friday, 10th November, 2017
Second semester: Monday, 26th February, 2018 to Friday, 2nd March, 2018

**Examination dates**

Winter examinations for those modules taught fully in the first semester commence on Wednesday, 3rd January, 2018
Annual examinations commence on Monday, 30th April, 2018 and finish at the latest on Friday, 25th May, 2018 (check web portal)

**Electronic Engineering - Module Choices**

All students must take 4E1, 4C4, 4C5, 4C7, 4C16 and CS3421 in semester 1. Those students taking the project must also take 4C15 in semester 2 and choose a two further modules from 4C8, CS4405, ME4B9 and ME4B12. All students must take 4C15 in semester 2 unless taking an internship.

**Electronic/Computer Engineering - Module Choices**

Each student must take 4E1, 4C4 and 4C5 in semester 1. Those students taking the project must choose a total of 6 modules from the following list (which must include at least 2 CS-coded modules) - 4C7, 4C16, CS4D2A, CS421, CS4052, CS4053, 4C8, 4C15, CS4D2B, CS4405, CS4407, CS7434, ME4B9 and ME4B12.

**Computer Engineering - Module Choices**

All students must take 4E1 and five other semester 1 modules totalling 30 credits from CS3421, CS4D2A, CS4052, CS4053, CS4400, CS4404, 4C4 and 4C16. Those students taking the project must also choose three semester 2 modules totalling 15 credits from CS4D2B, CS4405, CS4406, CS4407 and CS7434.

* For students who are completing their studies in the Senior Sophister BAI year, they MUST undertake a Computer/Electronic Engineering Project - the option of taking an Internship is only available to students proceeding to the MAI year 5.