<table>
<thead>
<tr>
<th>DAY</th>
<th>SEMESTER</th>
<th>0900 - 1000</th>
<th>1000 - 1100</th>
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<tbody>
<tr>
<td>WEDNESDAY</td>
<td>First semester</td>
<td>3E1 tutorial [CEDR and DO]</td>
<td>3E3 [CHLLT] (weeks 1 - 9) [2043] (weeks 10 - 12)</td>
<td>3D5A [LB01]</td>
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<td></td>
<td>Second semester</td>
<td>3C5 [HAM2]</td>
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<td>3C3 [CLT]</td>
<td>3D2 [LB08]</td>
<td>3D3 [LB04]</td>
<td>3C7 laboratory session [CADLAB]</td>
<td>CS2022 [HAM3]</td>
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<tr>
<td>FRIDAY</td>
<td>First semester</td>
<td>3E1 [2043]</td>
<td>3C1 [2039]</td>
<td>3E3 [2039]</td>
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<td>3D5A laboratory sessions [LG12]</td>
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<td>Second semester</td>
<td>3C3 [CLT]</td>
<td>3D2 [LB04]</td>
<td>3C5 [M20]</td>
<td></td>
<td>3D5B laboratory sessions [LG12]</td>
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<td>3C8 laboratory sessions [CADLAB, UG LAB]</td>
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Module codes:
3E1 = MA3E1 Engineering Mathematics V [5 credits]
3E3 = EE3E3 Probability and Statistics [5 credits]
3E4 = CE3E4 Innovation and Entrepreneurship for Engineers [5 credits]
3C1 = EE3C01 Signals and Systems [5 credits]
3C2 = EE3C02 Digital Circuits [5 credits]
3C3 = EE3C03 Analogue Circuits [5 credits]
3C5 = EE3C05 Telecommunications [5 credits]
3C7 = EE3C07 Digital Systems Design [5 credits]
3C8 = EE3C08 Digital Circuits Design* [5 credits]
3C9 = EE3C09 Analogue Circuits Design* [5 credits]
3D1 = CS3D1 Microprocessor Systems 1 [5 credits]
3D2 = CS3D2 Microprocessor Systems 2 [5 credits]
3D3 = CS3D3 Computer Networks [5 credits]
3D4 = CS3D4 Operating Systems and Concurrent Systems [5 credits]
3D5A = CS3D5A Data Structures and Algorithms [5 credits]
3D5B = CS3D5B Software Design and Implementation* [5 credits]
CS2022 = CS2022 Computer Architecture II [5 credits]

Venues:
2039 = JM Synge Lecture Theatre, Arts Building
2043 = Thomas Davis Theatre, Arts Building
AP2.03 = Room 2.03, Second Floor, Aras an Phiarsaigh
CADLAB = Room 2.28, Second Floor, Aras an Phiarsaigh
CEDR = Civil Engineering Demo. Room, Simon Perry Building
CHLLT = Chemistry Large Lecture Theatre, Chemistry Building
CHSCLT = Science Lecture Theatre, Chemistry Building
CLT = Crossland Theatre, Parsons Building
DO = Drawing Office, Top Floor, Museum Building
HAM1 = Salmon Lecture Theatre, Hamilton Building
HAM2 = Synge Lecture Theatre, Hamilton Building
HAM3 = MacNeill Theatre, Hamilton Building
HAM4 = Joly Theatre, Hamilton Building
HAM5 = Maxwell Theatre, Hamilton Building
LB01 = Lecture Theatre 01, Lower Basement, Lloyd Institute
LB04 = Lecture Theatre 04, Lower Basement, Lloyd Institute
LB08 = Lecture Theatre 08, Lower Basement, Lloyd Institute
LB1.07 = Room 1.07, First Floor, Lloyd Institute
LG12/35/36 = PC Room 12/35/36, Lower Ground Floor, O'Reilly Institute
M17 = Museum 17, First Floor, Museum Building
M20 = Museum 20, First Floor, Museum Building
M21 = Museum 21, First Floor, Museum Building
RH = Regent House, Top Floor, Front Gate
UG LAB = Room 2.15, Second Floor, Aras an Phiarsaigh

* 3C8, 3C9 and 3D5B are based entirely on project work

Electronic Engineering Modules
3E1; 3E3; 3E4 (or Broad Curriculum module); 3C1; 3C2; 3C3; 3C5; 3C7; 3C8; 3C9; 3D1 and 3D5A

Computer Engineering Modules
3E1; 3E3; CE3E4 (or Broad Curriculum module); 3C1; 3C2; CS2022; 3D1; 3D2; 3D3; 3D4, 3D5A and 3D5B

Electronic/Computer Engineering Modules
3E1; 3E3; CE3E4 (or Broad Curriculum module); 3C1; 3C2; 3C3; 3C5; 3C7; 3C8; 3D1; 3D2; 3D3 and 3D5A

Semester dates:
First semester: Monday, 25th September, 2017 to Friday, 15th December, 2017
Second semester: Monday, 15th January, 2018 to Friday, 6th April, 2018

Project/Assignment Weeks:
First semester: Monday, 6th November, 2017 to Friday, 10th November, 2017
Second semester: Monday, 26th February, 2018 to Friday, 2nd March, 2018

Examination dates:
Annual examinations commence on Monday, 30th April, 2018 and finish at the latest on Friday, 25th May, 2018 (check web portal)
Supplemental examinations commence on Saturday, 18th August, 2018 and finish on Saturday, 25th August, 2018 (to be confirmed)

FURTHER INFORMATION ON JUNIOR SOPHISTER ENGINEERING TIMETABLE, 2017/18